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Brennan 2-81-1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): J.M. Brennan et al.  
Case: 2-81-1-4  
Serial No.: 10/773,614  
Filing Date: February 6, 2004  
Group: 2823  
Examiner: Brook Kebede

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:  Date: September 27, 2005

Title: Semiconductor Device Configured for  
Reducing Post-Fabrication Damage

AMENDMENT TRANSMITTAL

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith is the following document relating to the above-identified patent application:

(1) Amendment and Response to Office Action.

There is no additional fee due in conjunction with the Amendment. In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

Respectfully submitted,



Date: September 27, 2005

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**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the non-final Office Action dated June 28, 2005, please amend the above-identified application as follows: